

## **PATENT APPLICATION**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tatsuo ITO et al.

Group Art Unit: 2125

Application No.: 10/502,389

Examiner:

M. VON BUHR

Filed: July 26, 2004

Docket No.: 120503

For:

METHOD FOR MANUFACTURING SEMICONDUCTOR WAFER, METHOD FOR RECEIVING ORDER FOR MANUFACTURE OF SEMICONDUCTOR WAFER, AND SYSTEM FOR RECEIVING ORDER FOR MANUFACTURE OF SEMICONDUCTOR

WAFER

## <u>AMENDMENT</u>

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reply to the March 20, 2006 Office Action, please consider the following:

Amendments to the Claims as reflected in the listing of claims; and

Remarks.